

**PATENT APPLICATION  
DOCKET #1263-0025US**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of:

DEEPAK SABHARWAL et al.

Serial No.: Unassigned

Filed: March 30, 2003

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Group Art Unit: Unassigned

Examiner: Unassigned

For: WORDLINE-BASED SOURCE-BIASING SCHEME FOR REDUCING MEMORY CELL  
LEAKAGE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

"EXPRESS MAIL" Mailing Label No..EV386626818US...  
Date of Deposit....March 30, 2004.....

Sir:

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Respectfully submitted,



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Date: March 30, 2004

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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	
		Filing Date	March 30, 2004
		First Named Inventor	Deepak Sabharwal et al.
		Art Unit	
		Examiner Name	
Sheet 1	of 1	Attorney Docket Number	1263-0025US

OTHER PRIOR ART–NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		KENICHI OSADA et al.; "16.7fA/cell Tunnel-Leakage-Suppressed 16MB SRAM for Handling Cosmic-Ray-Induced Multi-Errors"; 2003 IEEE International Solid-State Circuits Conference; Pages 302-303.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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